

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/619,594	07/16/2003	Tomohiko Otose	8070-1001 7456	
466 7	7590 02/15/2005		EXAM	INER
YOUNG & THOMPSON			MOUTTET, BLAISE L	
745 SOUTH 2:	3RD STREET			
2ND FLOOR			ART UNIT	PAPER NUMBER
ARLINGTON, VA 22202			2853	
		DATE MAILED: 02/15/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

	_	ri
(,	1
<u>_</u>	/	

	Application No.	Applicant(s)				
	10/619,594	OTOSE, TOMOHIKO				
Office Action Summary	Examiner	Art Unit				
	Blaise L. Mouttet	2853				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period was provided to the period for reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	of(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days till apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONEC	ely filed swill be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).				
Status						
 1) ⊠ Responsive to communication(s) filed on 16 Ju 2a) ☐ This action is FINAL. 2b) ⊠ This 3) ☐ Since this application is in condition for allowant closed in accordance with the practice under E 	action is non-final. ace except for formal matters, pro					
Disposition of Claims						
4) ⊠ Claim(s) <u>1-61</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) □ Claim(s) is/are allowed. 6) □ Claim(s) is/are rejected. 7) □ Claim(s) is/are objected to. 8) ⊠ Claim(s) <u>1-61</u> are subject to restriction and/or expressions.						
Application Papers						
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the confidence of Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Examine 11.	epted or b) objected to by the Edrawing(s) be held in abeyance. See on is required if the drawing(s) is obj	ected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list of the certified copies of the priority 	s have been received. s have been received in Application ity documents have been receive (PCT Rule 17.2(a)).	on No d in this National Stage				
Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa					
S. Patent and Trademark Office						

Application/Control Number: 10/619,594

Art Unit: 2853

Election/Restrictions

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 1-29 and 57, drawn to a semiconductor device and a driving method in which an identical data signal is successively provided over a plurality of times from an identical address in a memory unit, classified in class 365, subclass 189.01.
- II. Claims 30-56, drawn to an image output device provided with an injector for firing a droplet for printing, classified in class 347, subclass 5.
- III. Claims 58-61, drawn to a manufacturing method, of a semiconductor device or image output device in which the elements of the device are formed in a one and the same process classified in class 438, subclass 21.

Inventions I and II are related as subcombination and combination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because the identical control signal of Invention II need not be provided from an identical address as required from Invention I. The subcombination has separate utility such as in driving functional elements other than those used in firing droplets for printing such as driving LEDs of a display or elements involved in memory storage.

Inventions I and III are related as product made and process of making. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the product as claimed may be made by a combination of different semiconductor manufacturing processes (PVD, CVD, wet etching, dry etching, etc.).

Inventions II and III are related as product made and process of making. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the product as claimed may be made by a combination of different semiconductor manufacturing processes (PVD, CVD, wet etching, dry etching, etc.).

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

A telephone call was made to applicant's representative on February 11, 2005 to request an oral election to the above restriction requirement, but did not result in an election being made.

Application/Control Number: 10/619,594 Page 4

Art Unit: 2853

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Blaise Mouttet who may be reached at telephone number (571) 272-2150. The examiner can normally be reached on Monday-Friday from 8:30 a.m. to 5:00 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Meier, Art Unit 2853, can be reached at (571) 272-2149. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Blaise Mouttet February 11, 2005

Rleis Mouth 2/11/2005